

Exploring Memory Hierarchy Design With Emerging Memory Technologies (Lecture Notes In Electrical Engineering) By Guangyu Sun

By Guangyu Sun

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Guangyu Sun Receives EDAA Outstanding Dissertation -

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Exploring Latency-Power Tradeoffs in Deep -

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Guangyu Sun - DBLP -

scheduled maintenance of this server on Thursday, July 2, 2015. While we are working, this server may become unreachable for short periods of time. In the mean time
<http://dblp.uni-trier.de/pers/hd/s/Sun:Guangyu>

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Processing-in- Memory: Exploring the Design Space -

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Chapter 5 - The Tools Perspective on Software -

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<http://www.sciencedirect.com/science/article/pii/S0031320313002537>