

# **Exploring Memory Hierarchy Design With Emerging Memory Technologies (Lecture Notes In Electrical Engineering) By Guangyu Sun**

**By Guangyu Sun**

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Designing for Emerging Technologies: Guangyu Sun - Exploring Memory Hierarchy Design with Emerging Technologies and Circuits (Lecture Notes in <http://avxsearch.se/?q=Designing%20for%20Emerging%20Technologies>

## **Chip Multiprocessor Design Space Exploration -**

as observed in the memory hierarchy of contemporary chip independent way which enables exploring a large memory hierarchy design space from a <http://www.computer.org/doiresolver/10.1109/TC.2009.77>

## **Energy Saving Techniques for Phase Change Memory -**

Techniques\_for\_Phase\_Change\_Memory In new avenues of design cache and memory hierarchy. Processing Technologies, ser. Lecture Notes in [http://www.academia.edu/4249420/Energy\\_Saving\\_Techniques\\_for\\_Phase\\_Change\\_Memory\\_PCM](http://www.academia.edu/4249420/Energy_Saving_Techniques_for_Phase_Change_Memory_PCM)

## **Exploring Memory Hierarchy Design with Emerging -**

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Springer Natural Sciences & Engineering Preview - Top Frontlist October December 2013 Drug Design Methodology, Concepts, and Mode-of-Action N/A eReference work

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**Efficient memory partitioning for parallel data** -

Certifying an electrical/electronic system as Nonvolatile memory technologies such and Spin-Transfer Torque Random Access Memory (STT-RAM) are emerging as

<http://dl.acm.org/citation.cfm?doid=2744769.2744831>

**Chapter 5 - The Tools Perspective on Software** -

functionality with a cleaner design and different technologies. exploring tool requirements for the in-memory data structures of reverse engineering

<http://www.sciencedirect.com/science/article/pii/S0065245810790057>

**www.miau.ac.ir** -

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<http://www.miau.ac.ir/uploads/tir%20mahe/ebooks.xlsx>

**Energy Resources Engineering | Stanford University** -

preparing lecture notes, memory hierarchy, For TAs in Energy Resources Engineering. Course and lecture design and preparation;

<http://exploreddegrees.stanford.edu/schoolofearthsciences/energyresourcesengineering/>

**Processing-in- Memory: Exploring the Design Space** -

Processing-in-Memory: Exploring the Design Space Marko Scrbak 1, Mahzabeen Islam , This includes lookups in the cache hierarchy, memory controller overhead, multi-

<http://www.csrl.unt.edu/~kavi/Research/ARCS-2015.pdf>

**CiteSeerX Citation Query Exploring Memory** -

Exploring Memory Hierarchy with ArchC (2003) by P Viana, E Barros, S Rigo Proceedings of the conference on Design, Automation and Test in Europe, 2005 "

<http://citeseerx.ist.psu.edu/showciting?cid=5099806>

**A Survey of Power Management Techniques for Phase** -

A Survey of Power Management Techniques for Phase Change Memory. Processing Technologies, volume 6965 of Lecture Notes in Emerging Technologies for

[http://www.academia.edu/10614857/A\\_Survey\\_of\\_Power\\_Management\\_Techniques\\_for\\_Phase\\_Change\\_Memory](http://www.academia.edu/10614857/A_Survey_of_Power_Management_Techniques_for_Phase_Change_Memory)

### **CiteSeerX Exploring Memory Hierarchy with ArchC -**

BibTeX @INPROCEEDINGS{Viana03exploringmemory, author = {Pablo Viana and Edna Barros and Ro Rigo and Rodolfo Azevedo}, title = {Exploring Memory Hierarchy with ArchC}  
<http://citeseerx.ist.psu.edu/viewdoc/summary?doi=10.1.1.472.3539>

### **Guangyu Sun Receives EDAA Outstanding Dissertation -**

(EDAA) for his dissertation "Exploring Memory Hierarchy Design with Emerging Memory Technologies." The award will be presented in March at the  
<http://www.cse.psu.edu/news/2013/20130211-1>

### **dblp: Lecture Notes in Electrical Engineering -**

Bibliographic content of Lecture Notes in Electrical Engineering. Guangyu Sun: Exploring Memory Hierarchy Design with Emerging Memory Advanced Technologies,  
<http://dblp.uni-trier.de/db/series/lnee/index>

### **Exploring memory hierarchy design with emerging -**

Exploring memory hierarchy design with emerging memory technologies. [Guangyu Sun] Lecture notes in electrical engineering, v. 267.

<http://www.worldcat.org/title/exploring-memory-hierarchy-design-with-emerging-memory-technologies/oclc/859335257>

### **Exploring Engineering An Introduction To -**

Exploring Engineering An Introduction To Engineering And Design Price comparison. Electrical & Electronics Project Design For Engineering

<http://finderscheapers.com/Search.aspx?kw=exploring+engineering+an+introduction+to+engineering+and+design&3045=book>

### **Exploring Memory Hierarchy with ArchC | Guido -**

Exploring Memory Hierarchy with ArchC ArchC was extended to support a more detailed description of the memory subsystem, allowing the design space exploration of

[http://www.academia.edu/5883888/Exploring\\_Memory\\_Hierarchy\\_with\\_ArchC](http://www.academia.edu/5883888/Exploring_Memory_Hierarchy_with_ArchC)

### **ISSCC 2014 Books List -**

Exploring Memory Hierarchy Design with Emerging Memory Technologies. Series: Lecture Notes in Electrical Engineering, Vol. 267. Sun, Guangyu 2014.

<http://www.springer.com/engineering/conferences+in+engineering/isscc+conference?SGWID=4-1752216-66-653429-0&sba=INCLUDE&originalID=562827&resultStart=31>

### **Guangyu Sun - DBLP -**

scheduled maintenance of this server on Thursday, July 2, 2015. While we are working, this server may become unreachable for short periods of time. In the mean time

<http://dblp.uni-trier.de/pers/hd/s/Sun:Guangyu>

### **Exploring a Multiprocessor Design Space to -**

Exploring a Multiprocessor Design Space to Analyze the Impact of Using STT-RAM in the Memory Hierarchy A THESIS SUBMITTED TO THE FACULTY OF THE GRADUATE SCHOOL  
[http://conservancy.umn.edu/bitstream/handle/11299/167286/Borse\\_umn\\_0130M\\_15431.pdf](http://conservancy.umn.edu/bitstream/handle/11299/167286/Borse_umn_0130M_15431.pdf)

### **TDGS - "Guangyu Sun" -**

"Guangyu Sun" Exploring Memory Hierarchy Design with Emerging Memory Technologies. , Lecture Notes in Electrical Engineering, ISBN:

<http://juliette.lsi.us.es/Bibliography.aspx?query=%22Guangyu+Sun%22>

### **Publications | VAST lab -**

Peng Li, Guangyu Sun, Designing Scratchpad Memory Architecture with Emerging STT-RAM Memory Technologies. 2011 (Lecture Notes in Computer

<http://vast.cs.ucla.edu/publications>

### **De-elastisation -**

"Performance analysis of asynchronous circuits using markov chains," ser. Lecture Notes in Tackling memory walls with emerging cache design in emerging

<http://dl.acm.org/citation.cfm?id=2755813>

### **Multiprocessor System-on-Chip Data Reuse Analysis -**

for Exploring Customized Memory Hierarchies\* Design. Keywords: Scratch pad memory management, customized memory hierarchy,

[http://www.ics.uci.edu/~dutt/pubs/C166\\_issenin.pdf](http://www.ics.uci.edu/~dutt/pubs/C166_issenin.pdf)

### **Guangyu Sun Exploring Memory Hierarchy Design with -**

Guangyu Sun Exploring Memory Hierarchy Design with Emerging Memory Technologies 123

<http://link.springer.com/content/pdf/10.1007/978-3-319-00681-9.pdf>

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<https://www.crcpress.com/High-Performance-Concrete-From-material-to-structure/Malier/9780419176008>

### **Deep learning - Wikipedia, the free encyclopedia -**

Another method is the long short term memory Lecture Notes in Computer Science 2766. G.Z. Sun, "Learning Context

[https://en.wikipedia.org/wiki/Deep\\_learning](https://en.wikipedia.org/wiki/Deep_learning)

### **TDGS - " Lecture Notes in Electrical Engineering" -**

"Lecture Notes in Electrical Engineering" Exploring Memory Hierarchy Design with Emerging Memory Technologies. Guangyu Sun.

<http://juliette.lsi.us.es/Bibliography.aspx?query=%22Lecture+Notes+in+Electrical+Engineering%22>

### **IEEE Xplore Abstract - Exploring memory hierarchy -**

Exploring memory hierarchy with ArchC ArchC was extended to support a more detailed description of the memory subsystem, allowing the design space exploration of

[http://ieeexplore.ieee.org/xpl/articleDetails.jsp?arnumber=1250315&sortType%3Dasc\\_p\\_Sequence%26filter%3DAND%28p\\_IS\\_Number%3A27982%29](http://ieeexplore.ieee.org/xpl/articleDetails.jsp?arnumber=1250315&sortType%3Dasc_p_Sequence%26filter%3DAND%28p_IS_Number%3A27982%29)

### **IEEE Xplore Abstract - Multiprocessor -**

Browse Conference Publications > Design analysis for exploring customized memory a wide range of customized memory hierarchy organizations

[http://ieeexplore.ieee.org/xpl/articleDetails.jsp?arnumber=1688758&sortType%3Dasc\\_p\\_Sequence%26filter%3DAND%28p\\_IS\\_Number%3A35624%29%26pageNumber%3D2](http://ieeexplore.ieee.org/xpl/articleDetails.jsp?arnumber=1688758&sortType%3Dasc_p_Sequence%26filter%3DAND%28p_IS_Number%3A35624%29%26pageNumber%3D2)

### **Pharmacy: What It Is and How It Works, Third -**

Pharmacy: What It Is and How It Works, Third Edition - CRC Press Book As the Electrical Engineering - Environmental Engineering - General Engineering

<https://www.crcpress.com/Pharmacy-What-It-Is-and-How-It-Works-Third-Edition/Kelly/9781439853054>

### **www.msrit.edu -**

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<http://www.msrit.edu/wp-content/uploads/2014/08/Springer-e-Books-to-VTU-Consortium.xls>

### **EXPLORING THE MEMORY HIERARCHY FOR PACKET -**

consumption and memory hierarchy delay are the overheads of 11 EXPLORING THE MEMORY HIERARCHY FOR PACKET PROCESSING APPLICATIONS Bahareh Safaei K

[http://www.academia.edu/11362739/EXPLORING\\_THE\\_MEMORY\\_HIERARCHY\\_FOR\\_PACKET\\_PROCESSING\\_APPLICATIONS](http://www.academia.edu/11362739/EXPLORING_THE_MEMORY_HIERARCHY_FOR_PACKET_PROCESSING_APPLICATIONS)

### **Exploring Latency-Power Tradeoffs in Deep -**

in Deep Nonvolatile Memory Hierarchies Doe Hyun Yoon doe-hyun.yoon@ We use the model to explore the cache hierarchy design space and present latency-power tradeo

[http://lph.ece.utexas.edu/users/dhyoon/pubs/mem\\_model\\_cf12.pdf](http://lph.ece.utexas.edu/users/dhyoon/pubs/mem_model_cf12.pdf)

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<http://www.amazon.co.uk/Guangyu-Sun/e/B00F01R7ZK>

### **Distributed and Fault-Tolerant Computing - Publications -**

Lecture Notes in Business Information Guangyu Sun, Ritu Design Exploration of Hybrid cache architecture with disparate memory technologies Xiaoxia Wu

[http://researcher.watson.ibm.com/researcher/view\\_grouppubs.php?grp=141](http://researcher.watson.ibm.com/researcher/view_grouppubs.php?grp=141)