

Lower Bounds On VLSI Implementations Of Communication Networks By Marc Snir

By Marc Snir

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Lower bounds for solving undirected graph -

both to prove stronger lower bounds on VLSI, [GICF] L.J. Guibas, H.T. Kung, C.D. Thompson, 'Direct VLSI Implementations for Combinatorial

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Communication lower bounds for distributed-memory -

memory nodes connected by a communication network. Chandra, and Marc Snir. Communication Communication lower bounds for distributed

http://www.academia.edu/543279/Communication_lower_bounds_for_distributed-memory_matrix_multiplication

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We present here the implementation of a parallel programming and messages received from the communication network into the queues and Marc Snir, Steve Otto

<http://www.sciencedirect.com/science/article/pii/S0167819196000993>

1 On the Energy Complexity of LDPC Decoder -

nds fundamental lower bounds on the energy of VLSI implementations of do not suggest the existence of any decoder implementations that reach these lower bounds.

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On the Complexity of VLSI Implementations and Graph Representations of than previous VLSI lower bounds for VLSI IMPLEMENTATIONS AND GRAPH

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<http://library.vu.edu.pk/cgi-bin/nph-proxy.cgi/000100A/http/dl.acm.org/citation.cfm=3fid=3d2486159.2486181&coll=3dDL&dl=3dGUIDE>

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On the complexity of VLSI implementations and graph representations of Boolean functions $i \leq n$, the following lower bounds are proved: any VLSI implementation

<http://ieeexplore.ieee.org/xpl/articleDetails.jsp?tp=&arnumber=73590&contentType=Journals+%26+Magazines&queryText%3DVLSI+Implementation>

Time Lower Bounds for Parallel Sorting on a -

Time Lower Bounds for Marc Snir: Pages: 329-338: Network Complexity of Sorting and Analysis of a Distributed Scheduler for Communication Networks:

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MPI: The Complete Reference - Netlib -

Parallel Virtual Machine-A User's Guide and Tutorial for Network Parallel The Complete Reference by Marc Snir and was printed and bound in the United

<http://www.netlib.org/utk/papers/mpi-book/mpi-book>

News | USC Center For Energy Informatics -

These programmable platform FPGA s allow for the implementation of heterogeneous multi for VLSI circuits Marc Snir is director of the

<http://cei.usc.edu/news>

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Lower bounds for VLSI implementation of residue -

263 INTEGRATION Letter Lower bounds for VLSI implementation of residue number system architectures Magdy A. Bayoumi The Center for Advanced Computer Studies

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Baruch Schieber - Publications - IBM -

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<http://doi.acm.org/10.1145/165231.165254>

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<http://arxiv.org/pdf/1503.04963.pdf>

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and Marc Snir. Marc Snir. personal communication, A lower bound on the size of shellsort networks: R. Cypher: Pages: 58-63: doi>10.1145/72935.72942:
<http://doi.acm.org/10.1145/72935.72953>

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Ashok K. CHANDRA and Marc SNIR IBM of special purpose computation networks [1, 6, 16, 22, 25] and of VLSI lower bound on communication
<http://www.sciencedirect.com/science/article/pii/030439759090188N>

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VLSI (very large scale integration of the cost of computer implementation and both lower and worst-case upper bounds for VLSI circuit

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Organization of systems with bussed -

This thesis explores using busses in communication Organization of systems with bussed We explore how to efficiently permute data among VLSI chips

http://www.academia.edu/2894006/Organization_of_systems_with_bussed_interconnections

"On the Complexity of VLSI Implementations and -

Abstract or Description. This paper presents lower bound results on Boolean function complexity under two different models. The first is an abstraction of tradeoffs

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<http://link.springer.com/article/10.1007/BF00209909>

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<http://insidehpc.com/2010/06/rock-stars-of-hpc-marc-snir/>

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A time complexity lower bound for randomized implementations of some shared objects. and Marc Snir.
Lower Bounds in Distributed Computing

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